

100

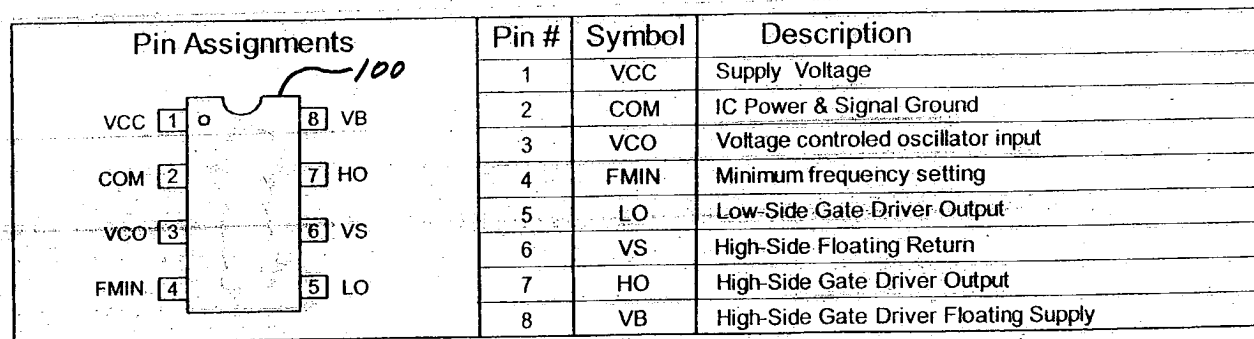
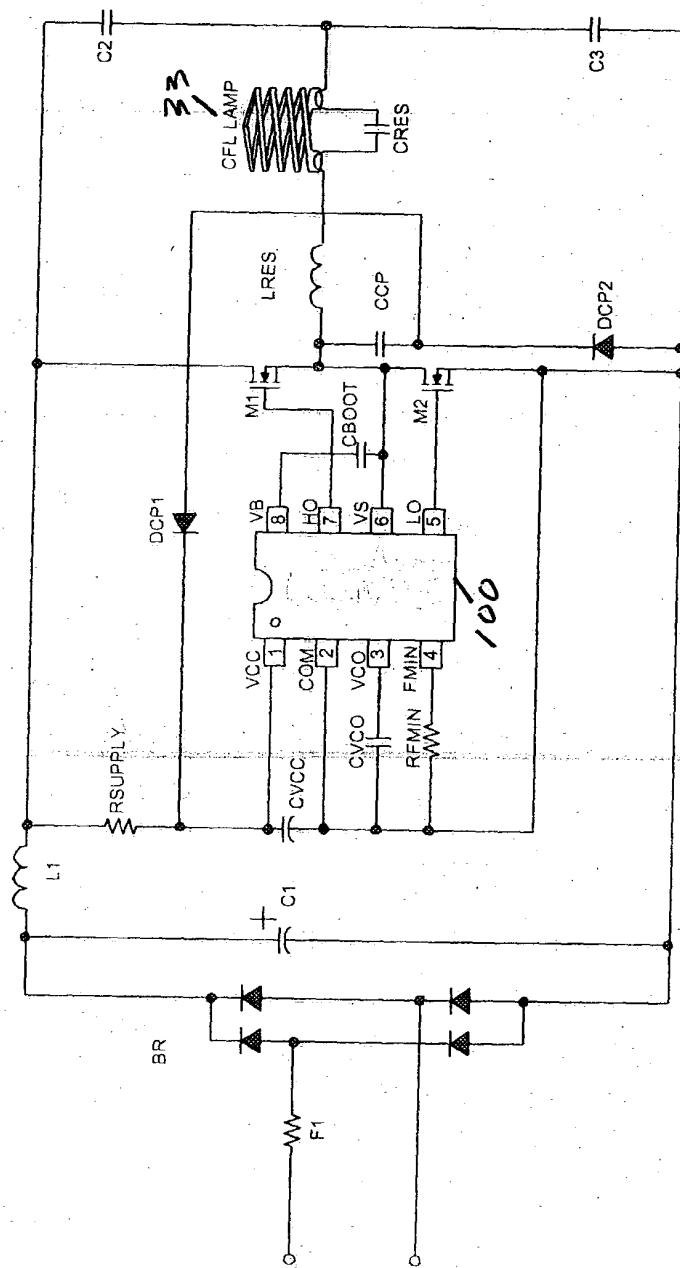


FIG. 2



200

Fig. 3

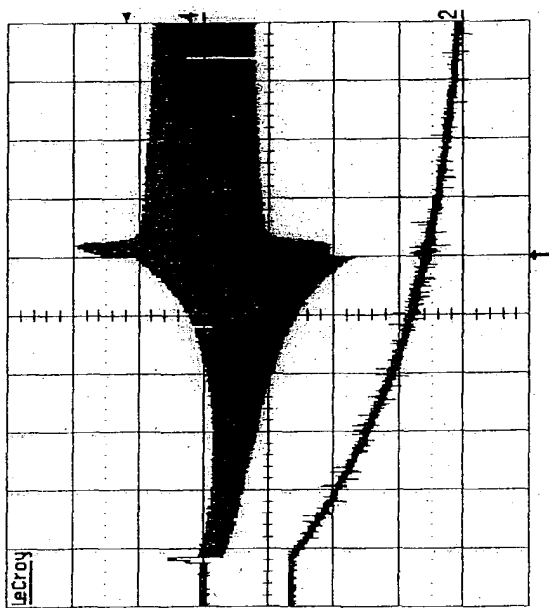
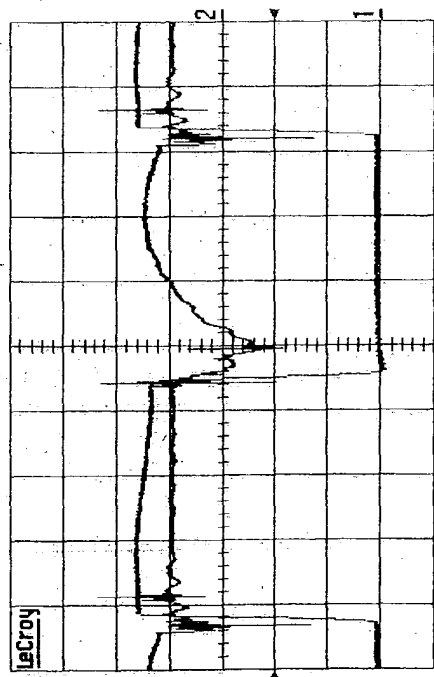
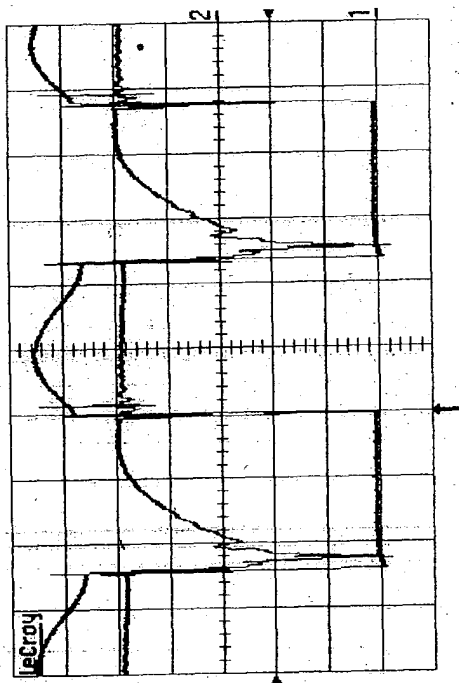


Fig. 4



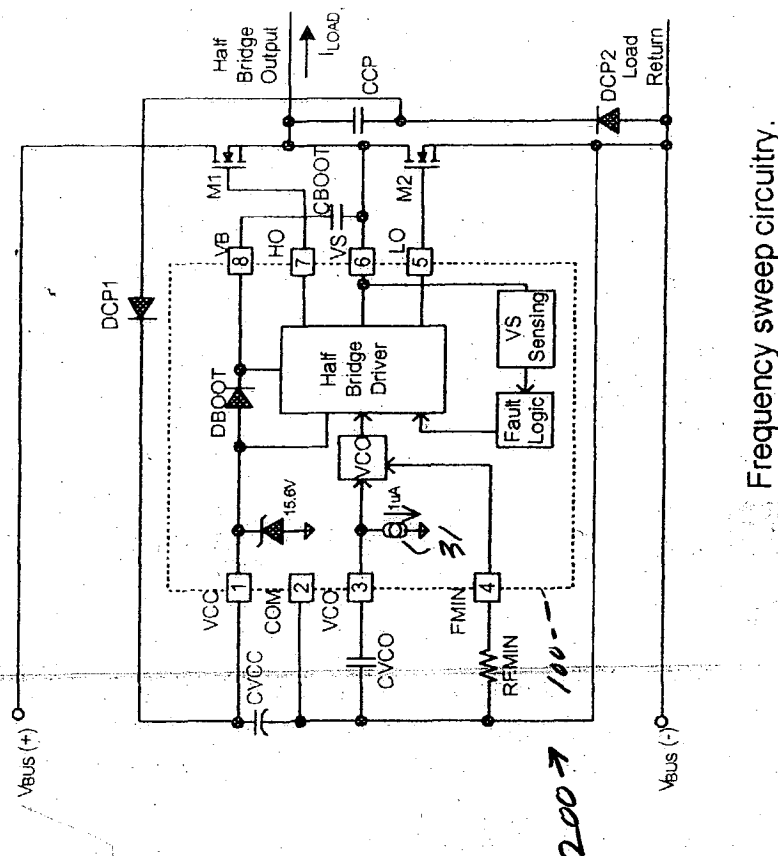


Fig. 8

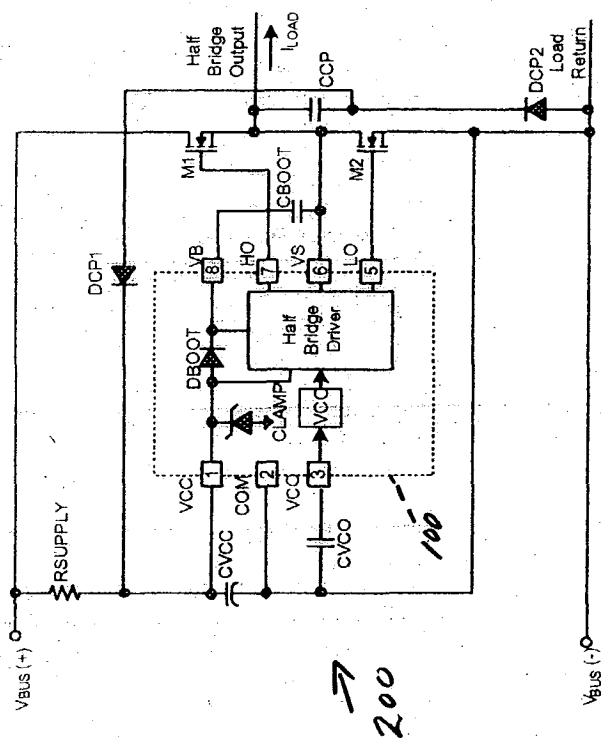
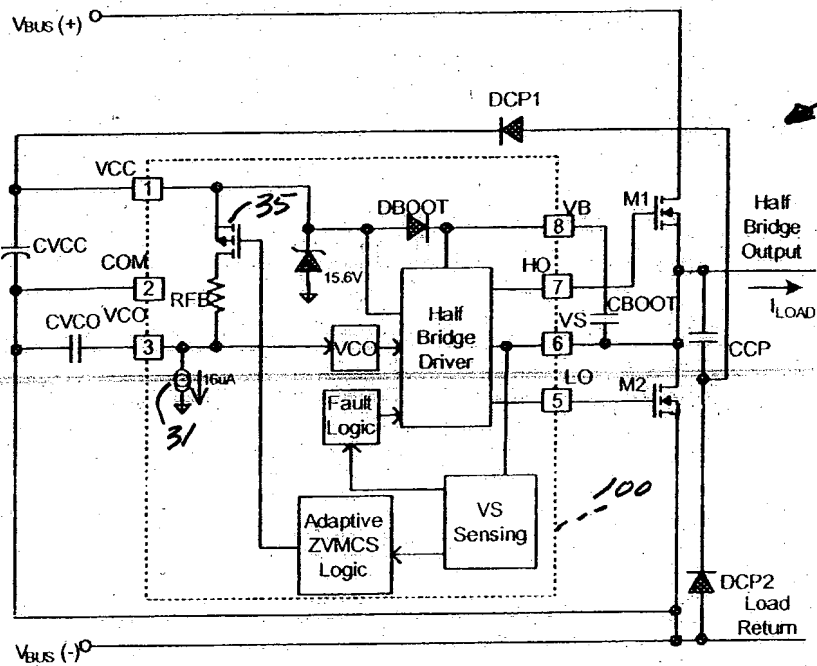


Fig. 7
UVLO circuitry



Adaptive ZVMCS Circuitry.

FIG. 9

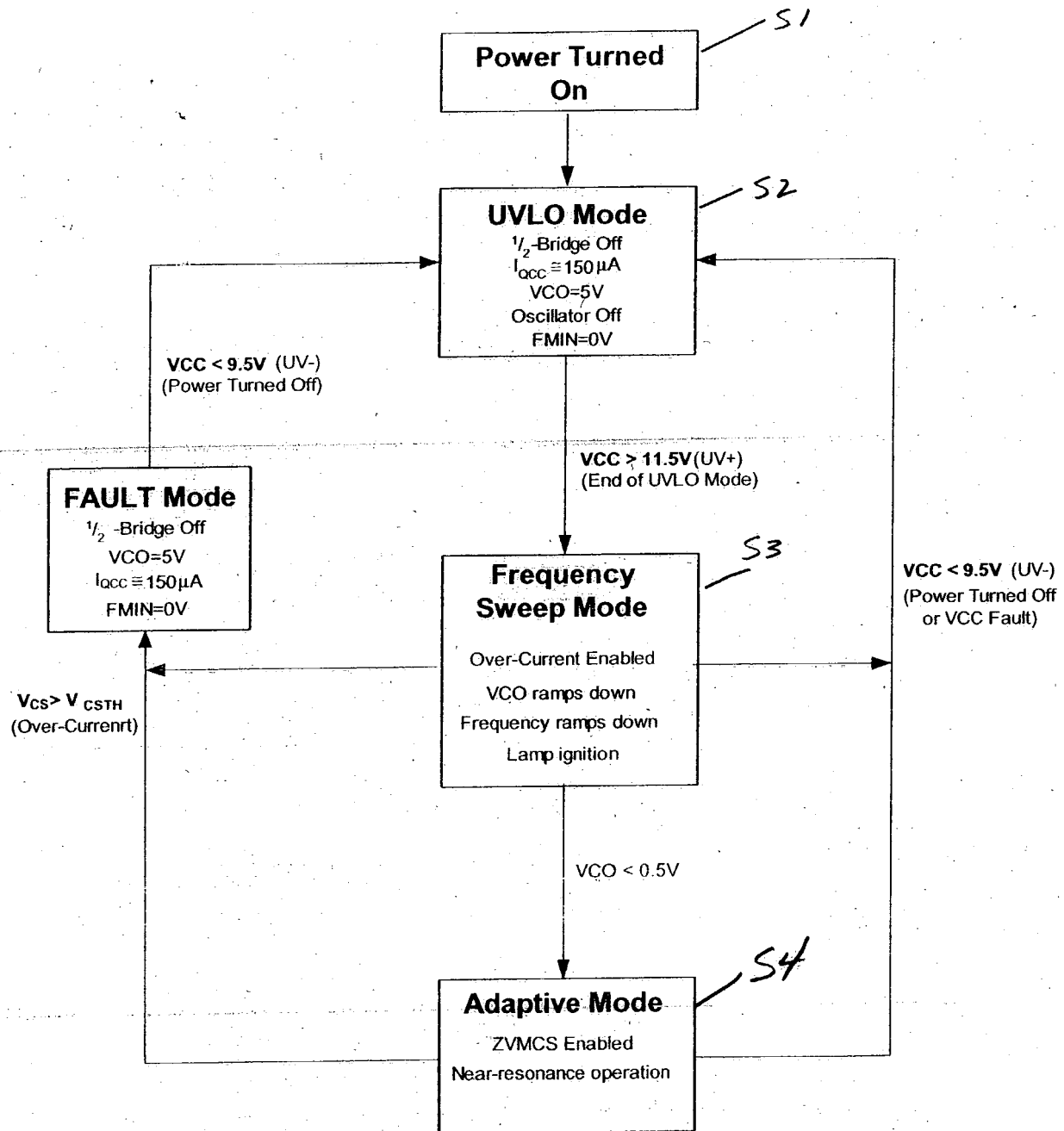


FIG. 10